

a plate between the first chip and the second chip, connected to the first chip and the second chip, and having two opposed longitudinal sides corresponding to the two longitudinal sides of the second chip, the plate defining a second length, the second length being larger than the first length to expose the opposed longitudinal sides of the plate.

6. (previously amended) The stacked semiconductor chip package according to claim 5, wherein corresponding to the two longitudinal sides of the second chip, the first chip has two opposed longitudinal sides defining a third length, and the third length is larger than the second length.

7. (previously amended) The stacked semiconductor chip package according to claim 5, wherein corresponding to the two longitudinal sides of the second chip, the second chip further has two opposed transverse sides defining a first width, the plate further has two opposed transverse sides defining a second width, the second width is smaller than the first width.

8. (previously amended) The stacked semiconductor chip package according to claim 7, wherein the first chip further has two opposed transverse sides defining a third width, and the second width is smaller than the third width.

9. (previously amended) The stacked semiconductor chip package according to claim 6, wherein corresponding to the two longitudinal sides of the second chip, the second chip further has two opposed transverse sides defining a first width, the plate further has two opposed transverse sides defining a second width, the second width is smaller than the first width.

10. (previously amended) The stacked semiconductor chip package according to claim 9, wherein the first chip further has two opposed transverse sides defining a third width, and the second width is smaller than the third width.

Please add the following claim:

11. (new) A stacked semiconductor chip package comprising:

a substrate;

a first chip on the substrate and electrically connected to the substrate by a plurality of electrical leads;

a second chip electrically connected to the substrate by a plurality of electrical leads and having two opposed longitudinal sides defining a first length; and

a plate between the first chip and the second chip, connected to the first chip and the second chip, and having two opposed longitudinal sides corresponding to the two longitudinal sides of the second chip, the plate defining a second length, the second length being larger than the first length to expose the opposed longitudinal sides of the plate and to expose an adhesive layer formed between the plate and the second chip.